

EL465851515

Priority Application Serial No. .... 09/141,775  
 Priority Filing Date ..... August 27, 1998  
 Inventor ..... Guy T. Blalock et al.  
 Assignee ..... Micron Technology, Inc.  
 Priority Group Art Unit ..... 1765  
 Priority Examiner ..... L. Vinh  
 Attorney's Docket No. .... MI22-1544  
 Title: Plasma Etching Methods



INFORMATION DISCLOSURE STATEMENT  
PURSUANT TO 37 C.F.R. §§1.56, 1.97 AND 1.98

In compliance with 37 C.F.R. §§1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a continuation of co-pending application Serial No. 09/141,775, filed August 27, 1998. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. §1.98(d) and MPEP §609(2). No admission is made regarding whether all the submitted references are prior art.

Citation of these references is respectfully requested.

Dated: Oct 2, 2000

Respectfully submitted,  
 Attorney: Bernard Berman  
 Bernard Berman  
 Reg. No. 37,279  
 WELLS, ST. JOHN, ROBERTS,  
 GREGORY & MATKIN P.S.

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-1544		SERIAL NO.	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Guy T. Blalock et al.		GROUP 1765 (priority)	
				FILING DATE Filed herewith			
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	5,879,575	3/99	Tepman et al.			
	AB	5,868,853	2/99	Chen et al.			
	AC	5,830,279	11/98	Hackenberg			
	AD	5,817,578	10/98	Ogawa			
	AE	5,788,869	8/98	Dalton et al.			
	AF	5,756,400	5/98	Ye et al.			
	AG	5,681,424	10/97	Saito et al.			
	AH	5,679,215	10/97	Barnes et al.			
	AI	5,644,153	7/97	Keller			
	AJ	5,626,775	5/97	Roberts et al.			
	AK	5,468,686	11/95	Kawamoto			
	AL	5,780,338	7/98	Jeng et al.			
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
	AM						
	AN						
	AO						
	AP						
	AQ						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
	AR		Stanley Wolf and Richard Tauber, Silicon Processing for the VLSI Era, Vol. 1, pp 549, 1986.				
	AS						
	AT						
EXAMINER				DATE CONSIDERED			
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>							

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APPLICANT Guy T. Blalock et al.		FILING DATE Filed herewith		GROUP 1765 (priority)	

  

U.S. PATENT DOCUMENTS							
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
AA	4,513,021	4/85	Purdes et al.				
AB	5,873,948	2/99	Kim				
AC	5,843,239	12/98	Shrotriya				
AD	5,716,494	2/98	Imai et al.				
AE	5,593,540	1/97	Tomita et al.				
AF	5,514,247	5/96	Shan et al.				
AG	5,242,538	9/93	Hamrah et al.				
AH	5,681,424	10/97	Saito et al.				
AI	4,528,066	7/85	Merkling, Jr. et al.				
AJ	5,843,226	12/98	Zhao et al.				
AK	6,093,655	7/00	Donohoe et al.				
AL	09/080,656		Tuman Earl Allen III				5/18/1998

  

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U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	09/429,880	10/99	Donohoe et al.			10/29/99
	AB	5,935,340	8/99	Xia et al.			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
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